



JMF100K207STU

FRED Modules

Features

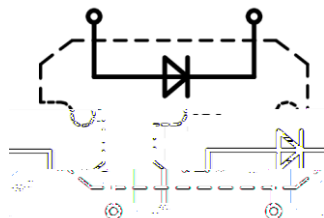
• e T eORd T_{JUSR}
4c T_{JUSR} Wac T_{JUSR} T_{JUSR} T_{JUSR}
 T_{JUSR} T_{JUSR} T_{JUSR} T_{JUSR}
DS g aV b SQ dS g PSV T_{JUSR}
DS g aV b SQ dS g bWS

Product Summary

Parameter	Value	Unit
$D_{@}$		D
$T_{4/D}$		/
T_{4A} . b a		/
D_{4} T_{4}		D
B Bg		a

Applications

E S T_{JUSR}
A A



Absolute Maximum Ratings . B_1 1 c T_{JUSR} bVS e VS a T_{JUSR}

Parameter	Conditions	Symbol	Values	Unit
$@$ S T_{JUSR} SOY SdS aS d T_{JUSR}	BdX 1	$D_{@}$		D
S S T_{JUSR} SOY SdS aS d T_{JUSR}	BdX 1	$D_{@A}$		D
/ dS T_{JUSR} ab T_{JUSR} S b	B_1 1 S RWRS	$T_{4/D}$		/
4 e ORac US T_{JUSR} S b	T_{JUSR} AWS e T_{JUSR} 6h BdX	T_{4A}		/
7b T_{JUSR} ST T_{JUSR} WJ		7b		/ a
@ A W T_{JUSR} d T_{JUSR}	/ 1 6h a W	$D_{7A=}$		D
8c T_{JUSR} bS S T_{JUSR} S O US		B_8		
Ab T_{JUSR} bS S T_{JUSR} S O US		B_{aU}		°C

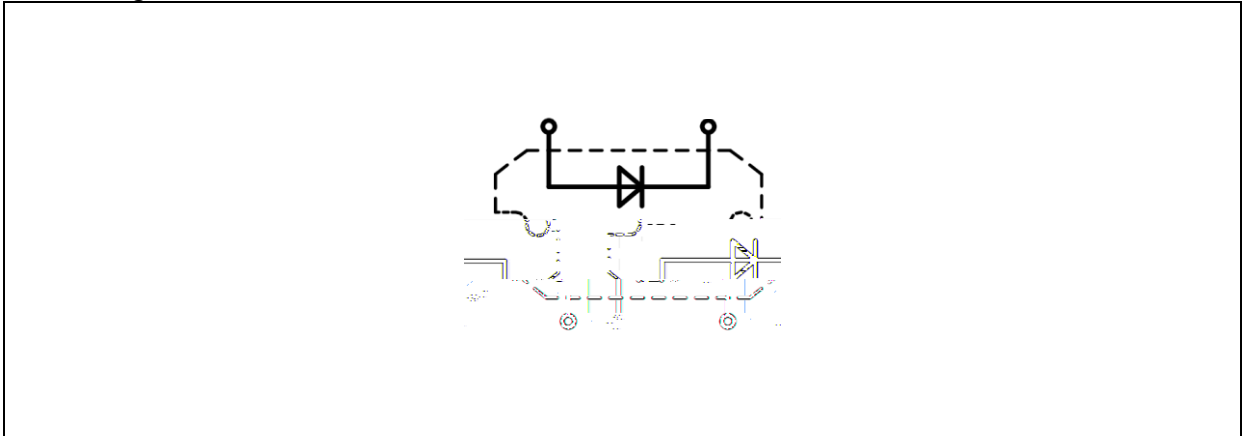


Electrical Characteristics

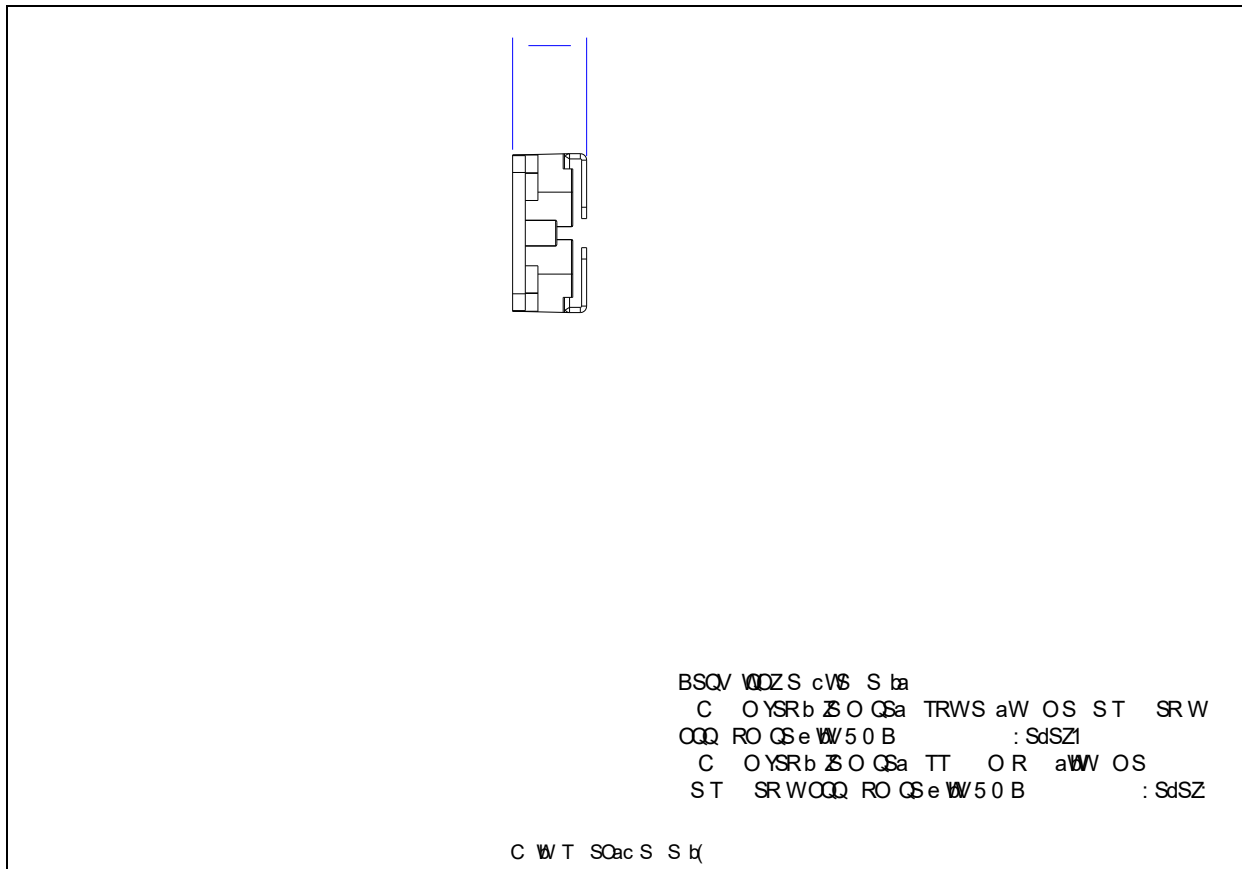
Parameter	Conditions	Symbol	Values			Unit
			Min.	Typ.	Max.	
4 eORd	74 / BdX	D4				D

D@ D@@
@SdS aS

Circuit Diagram



Package Outlines (mm)





7T CbW Tc WSR WbW/R Qc S b W/PSZSR b PS CQc CbS O R SZPZ 6 eSdS
8V Uac 8V8V AS V RcQ 1 :bR Qaac Sa Sa aWZT bVSQ aS cS QSa TcaS
eW cbQ aPS CbW T acQ/WT CbW caSPSg RWBVA/WT CbW WbW/R Qc S b
WacPSQbb QMO USE W cb W bOS be WabO RWU bW 8V Uac 8V8V e WZcZg Q Zg
eW bVS bS a cbZSR WO aW SR CU SS S b RcQa O R WT CbW dPSR WbW/
R Qc S b VOdS WTWUS S b T CbS ba 8V Uac 8V8V Qaac Sa Sa aWZT O g
WTWUS S b T bVS WMa TbWR ObSae WQ Og SacZT bVScaS TacQV RcQa O R
WT CbW BVA/R Qc S b WbVS WbWZ SZCaS RCbSR = Q PS BVA/R Qc S b
ac SaSRSa O R S ZCSa OZWT CbW SdWcaZac ZSR
W SUAS SR bORS OY T8V Uac 8V8V AS V RcQ 1 :b 8V Uac 8V8V
AS V RcQ 1 :bR / Z WMa SaS dSR