B 7& & 8

E H

8

E	8	H				
			B		B	
	℃					
	℃					
	℃					
	℃					
	℃					
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	℃					

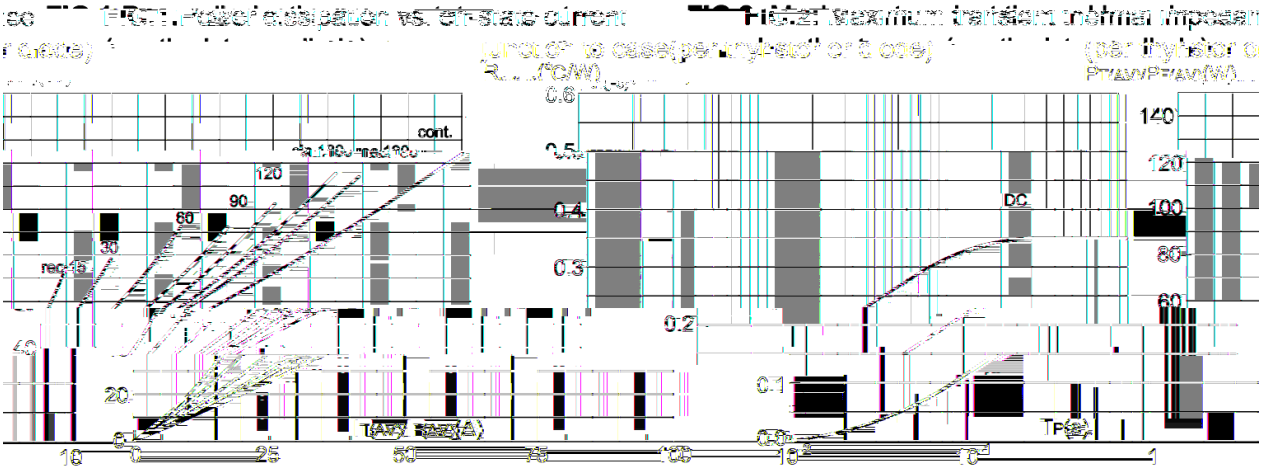
8

E	8	H				
			B		B	

D

	B	E		7	E 8

8



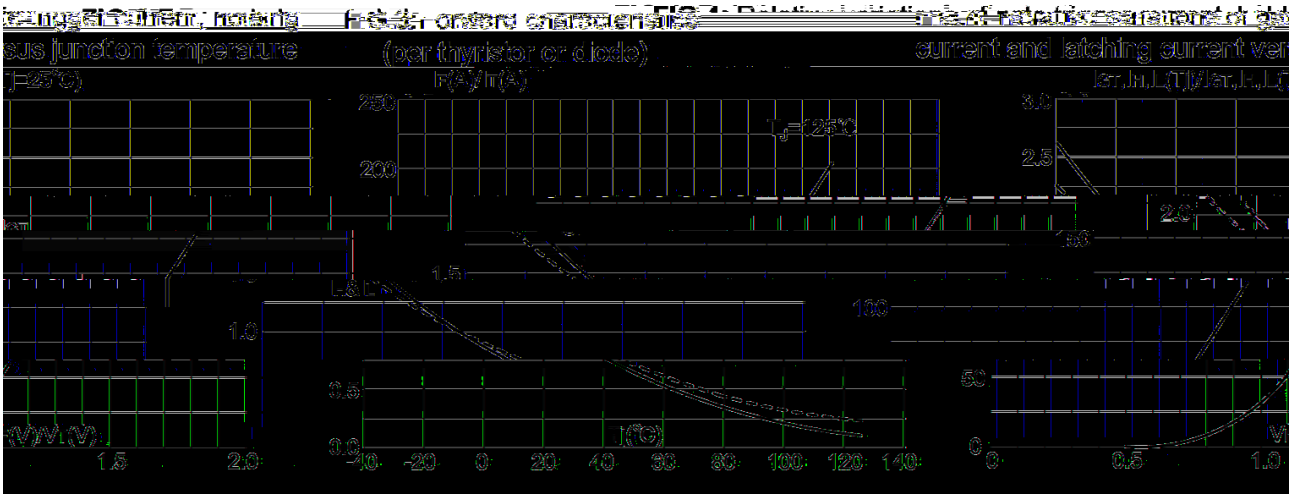


FIG.5 Gate trigger characteristics

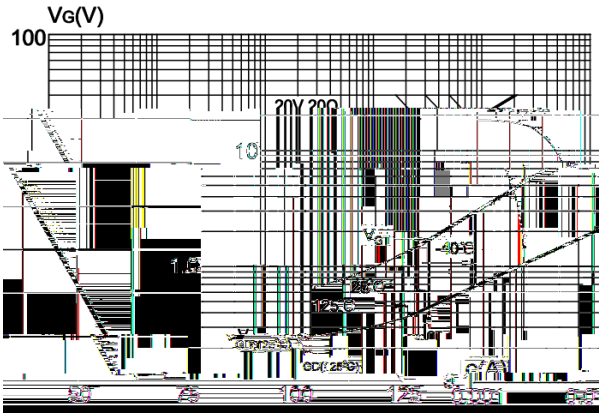
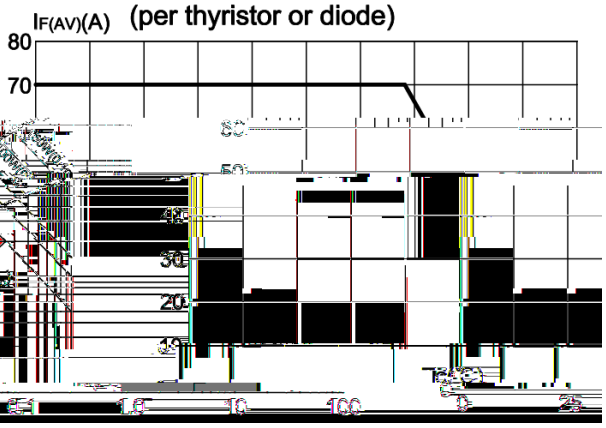


FIG.6: Forward current vs. case temperature (per thyristor or diode)



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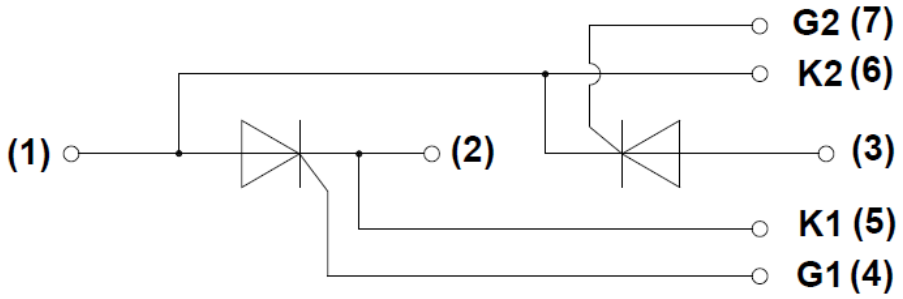
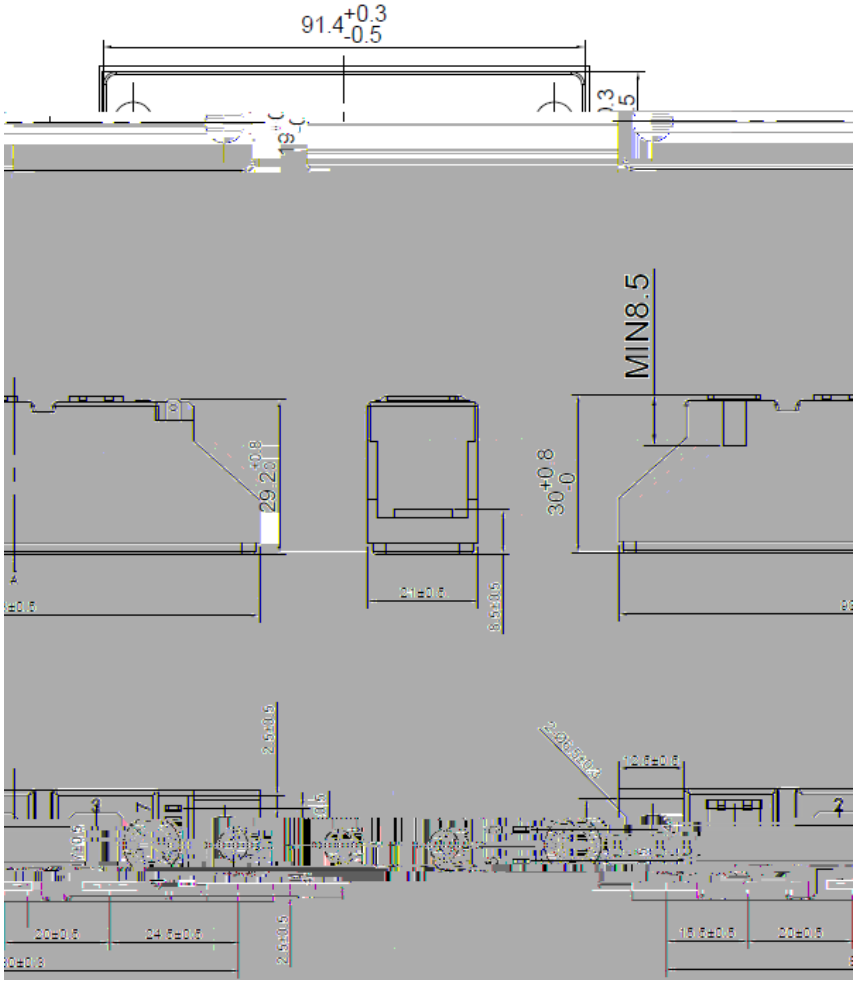
E D □